

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit includes a first controlling circuit
5 section, a level transforming circuit, a first buffer circuit, a second buffer circuit,
and an overvoltage protecting circuit. A first n-channel type MOS transistor is
provided in the first buffer circuit and a second p-channel type MOS transistor is
provided in the second buffer circuit. Thus, faster operation can be obtained, and
voltage between the source and drain of a third p-channel type MOS transistor
10 and a third n-channel type MOS transistor of the overvoltage protecting circuit,
impressed when output signal OUT changes, can be decreased.